

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF THE CLAIMS:**

1-7. (Canceled).

8. (Currently Amended) A network bridge comprising:

at least one arrangement for configuration and control of the network bridge;

[[and]]

at least one interface for providing access to at least some functional blocks of the network bridge for polling and evaluation of at least one of statistical data, performance data, operating data, and parameters, and for manipulation of the at least one of statistical data, performance data, operating data, and parameters, and of the functional blocks, as a function of the evaluation, including detecting a defect in or an attack from a device connected to the network bridge; and

situated above the software layer for configuration and control, at least one further software layer via which one of a network operator and a user can control functions of the network bridge;

wherein the at least one arrangement includes a software layer within a network bridge architecture.

9. (Previously Presented) The network bridge according to claim 8, wherein the network bridge is for coupling serial IEEE 1394 buses.

10. (Canceled).

11. (Canceled).

12. (Previously Presented) The network bridge according to claim 8, wherein a configuration of resources, including at least one of memory capacity and line capacity, is a function of varying operating parameters.

13. (Previously Presented) The network bridge according to claim 12, wherein an allocation of memory regions, including a temporary memory for data to be transported via the network bridge, is a function of a statistical evaluation of a data volume for different data types including at least one of asynchronous and isochronous data.

14. (Previously Presented) The network bridge according to claim 13, wherein, in the event of at least one of (a) a defect in one of a connected bus and network and (b) an attack by an unauthorized person, at least one of (c) a transfer of data is haltable and (d) one of a relevant bus and a connected device is deactivatable.

15. (Canceled).

16. (Previously Presented) The network bridge according to claim 8, wherein responsive to the detecting, the defect or attack is suppressed by removing a source device responsible for the defect or attack via an interface between the arrangement and a PHY configuration register.

17. (Previously Presented) The network bridge according to claim 8, wherein responsive to the detecting, the defect or attack is suppressed by stopping transmission of data packets between the network bridge and a source device responsible for the defect or attack.

18. (Currently Amended) The network bridge according to claim 8, wherein there is a detected attack and the detected [[the]] attack includes an unauthorized access to blocked memory regions.

19. (Previously Presented) The network bridge according to claim 12, wherein an allocation of memory regions, including a temporary memory for data to be transported via the network bridge, is a function of a statistical evaluation of a data volume for different data types including at least asynchronous data.